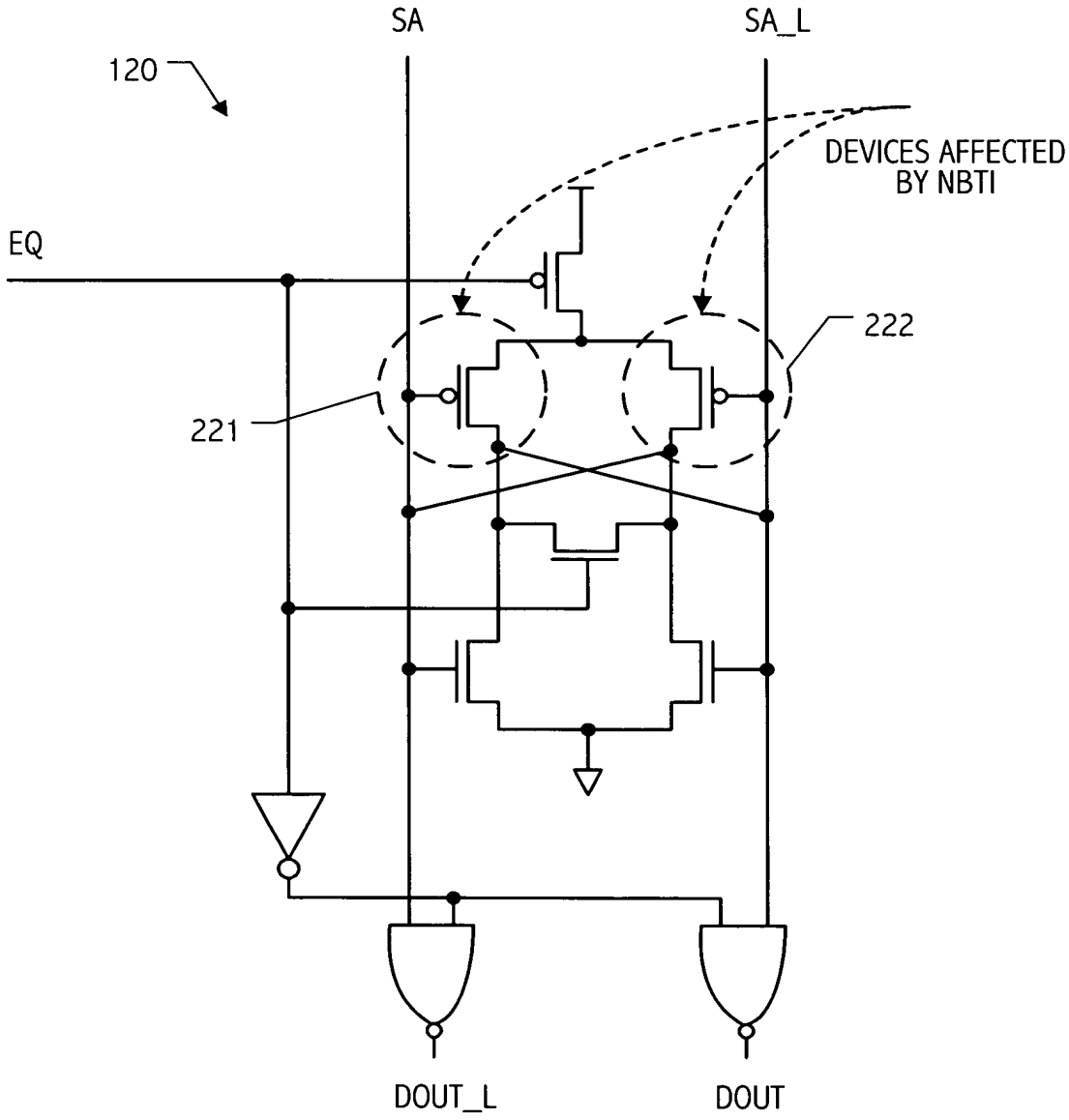

FIG. 2

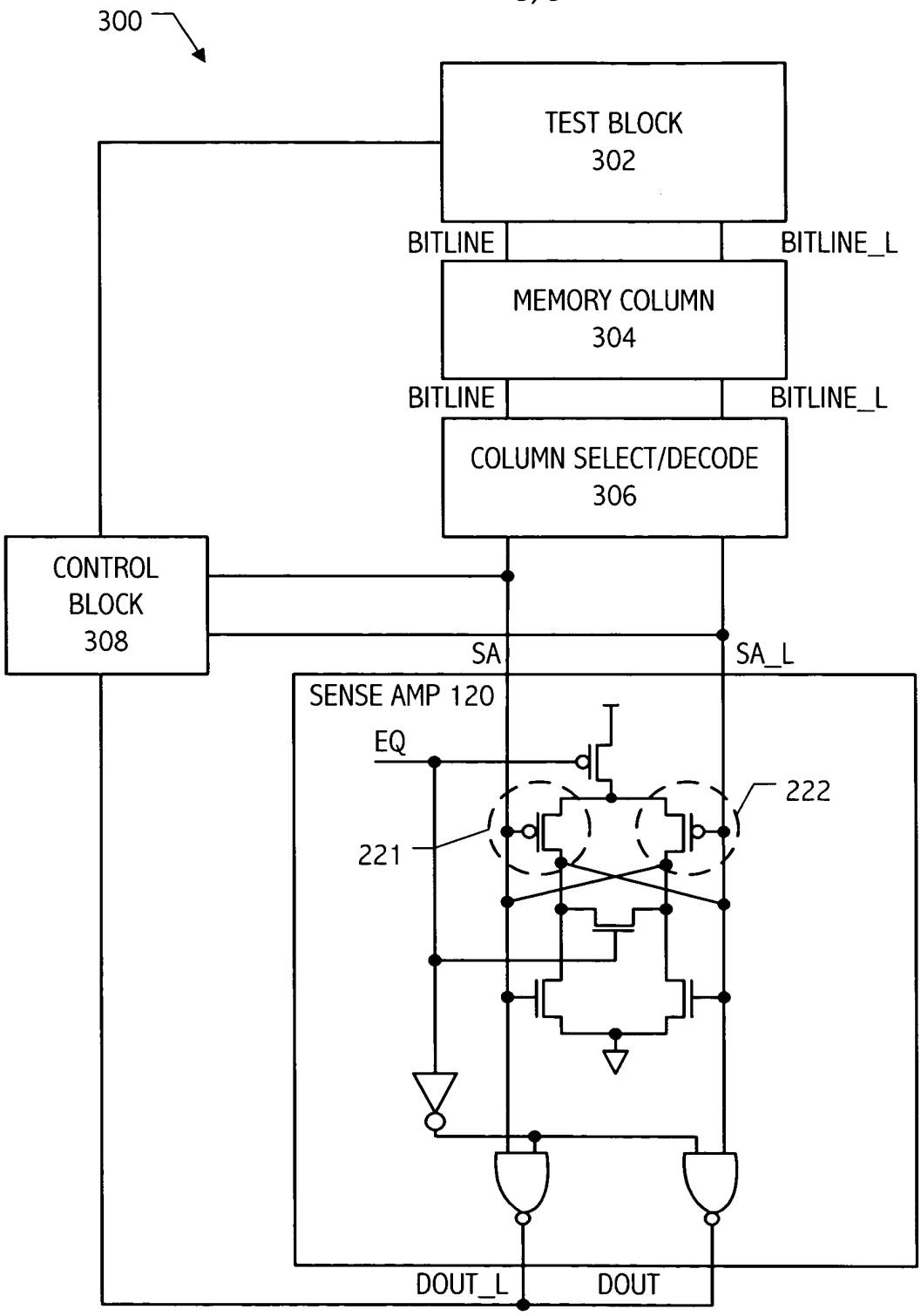
300
TEST BLOCK
302
BITLINE
BITLINE_L
MEMORY COLUMN
304
BITLINE
BITLINE_L
COLUMN SELECT/DECODE
306
CONTROL
BLOCK
308
SA
SA_L
SENSE AMP 120
EQ
222
221
DOUT_L
DOUT

FIG. 3

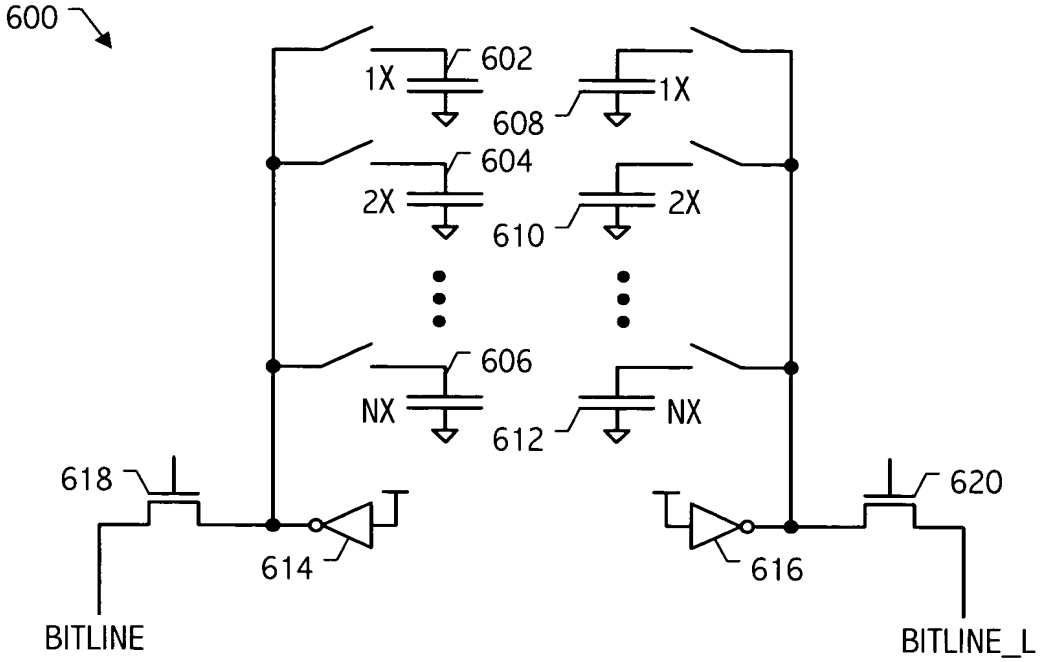
600
602
1X
608
1X
604
2X
610
2X
606
NX
612
NX
618
620
614
616
BITLINE
BITLINE_L

FIG. 6

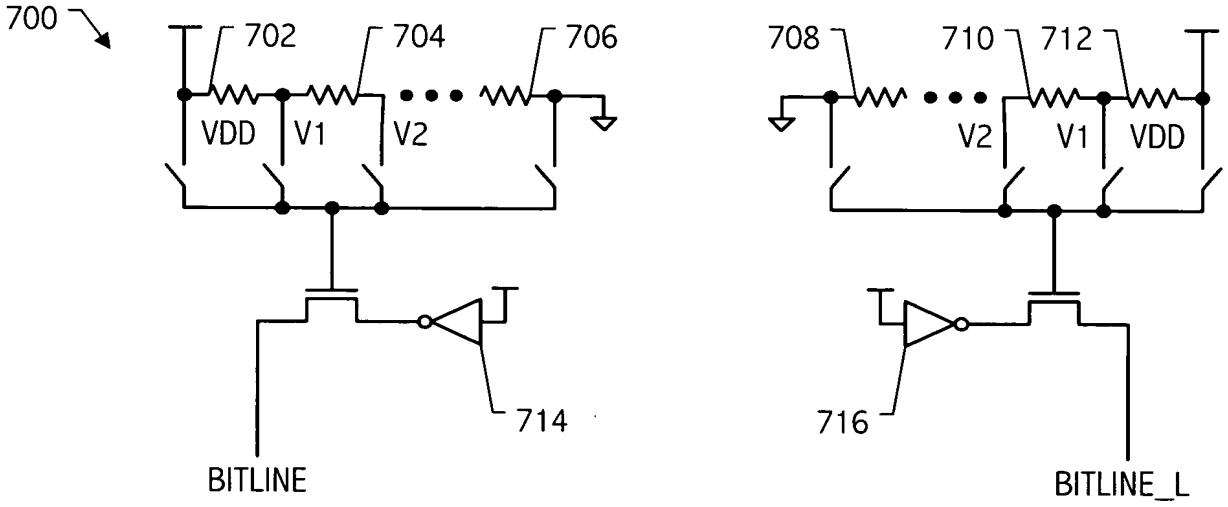
700
702
704
706
VDD
V1
V2
708
710
712
V2
V1
VDD
714
716
BITLINE
BITLINE_L

FIG. 7